

io-port 10967764

Agrawal, Vishwani D.; Bushnell, Michael L.; Parthasarathy, Ganapathy; Ramadoss, Rajesh
Digital Circuit Design for Minimum Transient Energy and a Linear Programming Method.

VLSI Design, 434-439(1999)

doi:10.1109/ICVD.1999.745194 <http://doi.ieeecomputersociety.org/10.1109/ICVD.1999.745194>