Kerjean, Sylvain; Kabanza, Froduald; St-Denis, Richard; Thiebaux, Sylvie
Analyzing LTL model checking techniques for plan synthesis and controller synthesis (work in progress).

Summary: We present alternative means of handling invariances in reachability testing, either by formula progression or compilation into Büchi automata. These alternatives are presented in connection with three different applications of model checking: verification, plan synthesis as well as heuristic guidance of AI planning, and controller synthesis. We include results from benchmarks obtained from preparatory experiments with model checking using a family of LTL2Büchi translators and formula progression.